

G O D W I N G R U B E R

GARY C. HONEYCUTT  
Direct Dial: 972.238.7160  
ghoneycutt@godwingruber.com

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ORIGINALLY FILED**

January 14, 2002

TC 2800 MAIL ROOM



2824X  
Attorneys  
A Professional Corporation

TELECOM CORRIDOR®  
801 E. Campbell Road, Suite 655  
Richardson, Texas 75081-1890  
972.238.7160  
972.238.7013 Fax

DALLAS  
214.939.4400

godwingruber.com

Assistant Commissioner for Patents  
Washington, D.C. 20231

Re: Patent Application For:  
**GOLD SPOT PLATED LEADFRAMES FOR SEMICONDUCTOR DEVICES  
AND METHOD OF FABRICATION**  
Serial No.: 09/525,105  
Attorney Docket No.: TI-28098  
Our File: 50000.2092

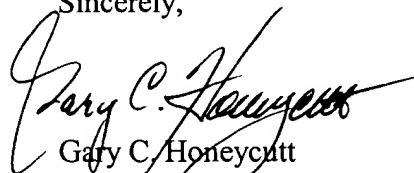
Dear Sir:

Enclosed for filing please find the following items relating to the above-identified application:

- (1) Amendment 37 CFR 1.115;
- (2) Replacement Pages;
- (3) One Month Extension of Time; and
- (4) Postcards.

Please file the above documents and return the date-stamped postcard to the corresponding addresses as indicated. In the meantime, if you have any questions or comments concerning this matter, please call the undersigned. Otherwise, please accept the enclosed.

Sincerely,

  
Gary C. Honeycutt  
Reg. No. 20,250

GCH/ecc

Encl.

cc: Lawrence Bassuk, Texas Instruments Incorporated